REMARKS

Applicant appreciates and acknowledges the withdrawal of the objection to the specification.

Applicant further appreciates and acknowledges the withdrawal of the 35 U.S.C. §112 and 35 U.S.C. §102 rejections asserted in the immediately prior Office Action and further appreciates the helpful comments of the Examiner.

Claims 1, 7, 10, 11, 17 and 20-22 are rejected under 35 U.S.C. §101 for being directed to non-statutory subject matter. The foregoing rejections are submitted to be overcome for the reasons set forth below.

The independent claims each include a preamble which sets forth a useful purpose. Also, the claim steps, e.g., mapping and selecting, are applied to accomplishing the useful purpose set forth in the preamble. For example, claim 1 includes:

"A method for simulating trace impedance for application to a printed circuit board design using a quasi-Monte Carlo model comprising:

generating a sequence of pseudo-random numbers according to a prescribed quasi-Monte Carlo model;

mapping each pseudo-random number R of the sequence of random numbers into multiple variables of unique values for the multi-dimensional space, the multi-dimensional space including D dimensions, where D is a number, the mapping providing a substantially even hard-coded point-cloud to define a grid of quasi-Monte Carlo points to provide some regularity in the point cloud over the multi-dimensional space; and
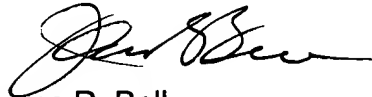
selecting a value of S according to a desired accuracy of a final simulation value, wherein the value of S defines a grid for use in conjunction with the mapping of the pseudo-random numbers into the multiple variables of the multi-dimensional space, wherein each of the multiple variables of the multi-dimensional space represents a corresponding D dimension value and wherein each dimension is characterized by a minimum and a maximum value, further wherein each dimension is characterized by a prescribed resolution S, and wherein S is the resolution of each dimension, and a ratio r, as defined by $r = s^D/P^N$ can be predetermined to be a prime number so that the value for S can be derived from the equation for r, whereby, the simulation value in the multi-dimensional space is reduced to provide an increased accuracy within a reduced time, the simulation value being applicable for simulations of trace impedance in circuit board design."

Independent claims 10, 11, 20, 21 and 22 include similar recitations.

The amendments herein are fully supported by the original specification and drawings, therefore no new matter is introduced.

In view of the above, it is respectfully submitted that claims 1, 7, 10, 11, 17 and 20-22 are in condition for allowance. Accordingly, an early Notice of Allowance for the remaining claims is courteously solicited.

Respectfully submitted,

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Dated: 3-3-05

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